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DRAM inventor

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The worlds first available **DRAM** chip In 1970 - the newly formed Intel company publically released the 1103 - the first **DRAM** memory chip and by 1972 it was ...

[inventors.about.com/library/weekly/aa100898.htm](http://inventors.about.com/library/weekly/aa100898.htm) - 37k - [Cached](#) - [Similar pages](#)

**Making Memories - History of DRAM**

Robert Dennard was the **inventor** of **DRAM** or Dynamic Random Access Memory. ...

Biography of Robert Dennard - **Inventor of DRAM** ...

[inventors.about.com/od/dstartinventors/a/DRAM.htm](http://inventors.about.com/od/dstartinventors/a/DRAM.htm) - 25k - [Cached](#) - [Similar pages](#)

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**Boston Business Journal: Lemelson-MIT Lifetime prize goes to DRAM ...**

Lemelson-MIT Lifetime prize goes to **DRAM inventor** Dennard ... **DRAM** chips store each bit of information as electrons in separate capacitors. ...

[www.bizjournals.com/boston/stories/2005/04/18/daily53.html](http://www.bizjournals.com/boston/stories/2005/04/18/daily53.html) - 50k -

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**Inventor of the Week: Archive**

In 1966, Dennard took the second step, when he **invented** one-transistor Dynamic Random Access Memory (or **DRAM**, better known simply as RAM). ...

[web.mit.edu/invent/iow/Dennard.html](http://web.mit.edu/invent/iow/Dennard.html) - 16k - [Cached](#) - [Similar pages](#)

**Inventor of the Week: Archive**

**DRAM**. Dennard Robert Heath Dennard **invented** in 1967 what is considered one of the most significant advances in computer technology: one-transistor dynamic ...

[web.mit.edu/invent/iow/dennard.html](http://web.mit.edu/invent/iow/dennard.html) - 15k - [Cached](#) - [Similar pages](#)

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**Micron Technology - Wikipedia, the free encyclopedia**

One of the most vicious was in 1985, when allegations of Japanese import dumping fueled a price collapse that caused **DRAM inventor** Intel to leave the market ...

[en.wikipedia.org/wiki/Micron\\_Technology](http://en.wikipedia.org/wiki/Micron_Technology) - 19k - [Cached](#) - [Similar pages](#)

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Robert Heath Dennard **invented** one-transistor Dynamic Random Access Memory (**DRAM**), which allowed major increases in computer memory density and decreases in ...

[www.invent.org/Hall\\_Of\\_Fame/1\\_1\\_6\\_detail.asp?vInventorID=41](http://www.invent.org/Hall_Of_Fame/1_1_6_detail.asp?vInventorID=41) - 8k -

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**IC Knowledge - History of the Integrated Circuit - 1970s**

Bob Dennard, the **inventor** of the 1 transistor **DRAM** cell was a strong proponent of that approach, but realized that a significant shrink of the cell size was ...

[www.icknowledge.com/history/1970s.html](http://www.icknowledge.com/history/1970s.html) - 17k - [Cached](#) - [Similar pages](#)

**Memory Chip Design Pioneer and Expert Witness**

First to employ "shared wordline" decoder for **DRAM** area saving. Co-inventor of world's first "folded bitline" patent; used in all modern DRAMs. ...

[www.circuitexpert.com/accomplish.html](http://www.circuitexpert.com/accomplish.html) - 11k - [Cached](#) - [Similar pages](#)

**Patents Results**

Techniques for reducing the amount of fuses in a **DRAM** with redundancy.. Document Type:

Application.. **Inventor**: Applicant: SIEMENS AKTIENGESSELLSCHAFT. ...

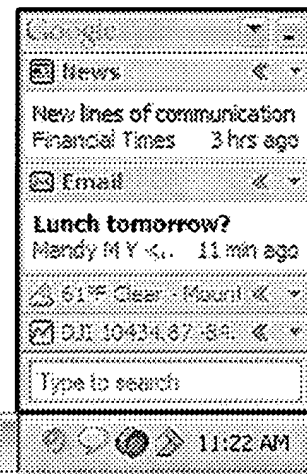
[patents.globalspec.com/Search?show=patents&query=dram](http://patents.globalspec.com/Search?show=patents&query=dram) - 50k -

<http://www.google.com/search?hl=en&q=DRAM+inventor>

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## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"10/607081"	US-PGPUB	OR	OFF	2006/04/06 17:03
S2	5	twin adj mode and display	US-PGPUB; USPAT	OR	OFF	2005/08/29 13:26
S3	49	345/3.2.ccls.	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:21
S4	2313	"spread spectrum modulation"	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:54
S5	16	"spread spectrum modulation" and LCD and CRT	US-PGPUB; USPAT	OR	OFF	2005/10/05 10:37
S6	142	"spread spectrum modulation" and LCD	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:36
S7	30	"spread spectrum modulation" and CRT	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:32
S8	507	S4 and buffer	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:33
S9	61	S4 and buffer and overflow	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:35
S10	220	S4 and buffer and feedback	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:35
S11	77	"spread spectrum modulation" and LCD and buffer	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:50
S12	541	"spread spectrum modulation" and (buffer FIFO)	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:50
S13	226	"spread spectrum modulation" and (buffer FIFO) and feedback	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:50
S14	172	"spread spectrum modulation" and (buffer FIFO) and feedback and (overflow full underflow)	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:51
S15	61	"spread spectrum modulation" and (buffer FIFO) and feedback and (overflow full underflow) and display	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:53
S16	160	345/3.1.ccls.	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:21
S17	2	345/3.1.ccls. and "spread spectrum modulation"	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:54
S18	72	345/3.1.ccls. and (buffer FIFO)	US-PGPUB; USPAT	OR	OFF	2005/09/29 16:56
S19	1	"5694141".pn.	USPAT	OR	OFF	2005/10/03 09:35

## EAST Search History

S20	36	("4751502"   "4760387"   "4980678"   "5124688"   "5138305"   "5150109"   "5222212"   "5293474"   "5293485"   "5327156"   "5479184"   "5488385").PN. OR ("5694141").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 09:35
S21	1	"6154225".pn.	US-PGPUB; USPAT	OR	OFF	2005/10/03 13:01
S22	8	("4393394"   "5218274"   "5473342"   "5488385"   "5510843"   "5694141").PN. OR ("6154225").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:03
S23	2731	buffer and clock near5 (change alter increase decrease) and display	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:04
S24	246	buffer same clock near5 (change alter increase decrease) and display	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:04
S25	27	buffer same clock near5 (change alter increase decrease) and CRT and LCD	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:45
S26	572	buffer same overflow and clock near4 (change alter raise lower increase decrease)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:46
S27	48	buffer same overflow same clock near4 (change alter raise lower increase decrease)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 13:46
S28	1	"6754745".pn.	USPAT	OR	OFF	2005/10/03 16:07
S29	6	("5619541"   "5631931"   "5764965"   "5793549"   "5870445").PN. OR ("6754745").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 16:16
S30	269	clock near4 synchroniz\$5 and pixel and display and buffer same overflow	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 16:17
S31	6	buffer same overflow same clock near4 synchroniz\$5 and pixel and display	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 16:25
S32	1	"4001690".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 16:25
S33	22	("3320611"   "3428898"   "3676599"   "3787634").PN. OR ("4001690").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/03 16:25

## EAST Search History

S34	73	clock same synchronize same (edges pulses) near4 count\$4 and buffer and display	US-PGPUB; USPAT	OR	OFF	2005/10/04 16:50
S35	73	clock same synchronize same ((edges pulses) near4 count\$4) and buffer and display	US-PGPUB; USPAT	OR	OFF	2005/10/04 16:50
S36	8	clock same synchronize same ((edges pulses) near4 count\$4) same buffer and display	US-PGPUB; USPAT	OR	OFF	2005/10/04 17:00
S37	144	clock same synchronize same ((edges pulses) near4 count\$4) and display	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:41
S38	337	clock near5 synchronize\$2 same ((edges pulses) near4 count\$4) and display	US-PGPUB; USPAT	OR	OFF	2005/10/04 17:00
S39	261	clock adj pulse\$2 near5 (count\$4) same synchronize	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:42
S40	108	clock adj pulse\$2 near5 (count\$4) same synchronize and display	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:42
S41	11	clock adj pulse\$2 near5 (count\$4) same synchronize same buffer and display	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:47
S42	0	clock near4 synchronize same clock adj pulses near5 count same compare	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:48
S43	2	clock near4 synchronize same clock adj pulses near5 count same compar\$4	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:50
S44	16	clock near4 synchronize same clock adj pulses near5 count	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:53
S45	48	clock adj3 frequency near6 (match shift change) and clock adj pulse near5 count	US-PGPUB; USPAT	OR	OFF	2005/10/05 09:54
S46	8	"spread spectrum modulation" and LCD and CRT and buffer	US-PGPUB; USPAT	OR	OFF	2005/10/05 10:37
S47	44	clock adj pulse near3 (count compar\$3) same first adj clock same second adj clock	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:19
S48	2459	clock adj pulse near3 (count compar\$3)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:19
S49	89	clock adj pulse near3 (count compar\$3) same buffer	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:19
S50	0	clock adj pulse near3 (count compar\$3) same buffer and LCD and CRT	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:19

## EAST Search History

S51	42	clock adj pulse near3 (count compar\$3) same buffer and (video display)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:21
S52	426	frequency near4 (adjust correct match synchronize) same clocks	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:23
S53	3	frequency near4 (adjust correct match synchronize) same clocks same pulse near4 (count compare)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:21
S54	4	frequency near4 (adjust correct match synchronize) same clocks same pulses near4 (count compare)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:22
S55	28	frequency near4 (adjust correct match synchronize) same clocks same (count compare)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:24
S56	75	clock adj2 frequency near4 matching	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:41
S57	7	clock adj2 frequency near4 matching and pulse near3 (counting comparing count compare)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:25
S58	2917	buffer same input same output same (match synchronize)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:42
S59	736	buffer same input same output same (match synchronize) same clock	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:42
S60	86	buffer same input same output same (match synchronize) same clock same (count compare)	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:59
S61	25437	center adj frequency	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:59
S62	174	center adj frequency near8 match	US-PGPUB; USPAT	OR	OFF	2005/10/05 13:59
S63	4	center adj frequency near8 match same count	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:00
S64	5	center adj frequency near8 match same count\$3	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:04
S65	105	buffer same (overflow underflow) and center adj frequency	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:04
S66	17	buffer same (overflow underflow) and center adj frequency same (adjust change match synchronize)	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:04
S67	0	345/3.2.ccls. and display adj pipe	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:21
S68	1	345/3.1.ccls. and display adj pipe	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:22

## EAST Search History

S69	81	display adj pipe	US-PGPUB; USPAT	OR	OFF	2005/10/05 14:22
S70	1976	clock adj pulses same frequency same equal	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:31
S71	1438	clock adj pulses same frequency same equal same (signal feedback)	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:31
S72	427	clock adj pulses same frequency same equal same (signal feedback) and display	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:32
S73	10	clock adj pulses same frequency same equal same (signal feedback) and display and LCD and CRT	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:40
S74	69	clock adj frequency same (match synchronize) same count and clock adj pulses	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:41
S75	0	clock adj frequency same (match synchronize) same count near5 clock adj pulses	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:41
S76	22	clock adj frequency same (match synchronize) and count near5 clock adj pulses	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:51
S77	338	(match synchronize) same clock adj pulses and clock adj frequency	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:52
S78	0	(match synchronize) same clock adj pulses and clock adj frequency and display	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:52
S79	143	(match synchronize) same clock adj pulses and clock adj frequency and display	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:55
S80	5	(match synchronize) same clock adj pulses and clock adj frequency and display and LCD and CRT	US-PGPUB; USPAT	OR	OFF	2005/10/06 09:52
S81	17	("3922485"   "4040096"   "4084197"   "4130838"   "4143403"   "4195316").PN. OR ("4320420").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:00
S82	70	buffer and clock and frequency near5 adjust and clock adj pulses near5 count	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:04
S83	566	"phase lock loop" and clock adj pulses and (synchronize match)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:05
S84	822	"phase lock loop" and clock adj pulses and (synchronize match)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:06

## EAST Search History

S85	971	"phase lock loop" and clock adj pulses and (synchronized matched)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:06
S86	185	"phase lock loop" and clock adj pulses same (synchronized matched)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:06
S87	94	"phase lock loop" and clock adj pulses near8 (synchronized matched)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:27
S88	8	clock adj pulses same "one-to-one" and frequency near8 (adjust synchronize match)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/06 10:27
S89	17	PLL and clock adj source and frequency near8 (adjust shift change) and clock near2 (pulse edge) near8 (compare count)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:40
S90	0	clock near2 (pulse edge) near8 average same compare and frequency near6 (adjust change alter)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:40
S91	0	clock near2 (pulse edge) near8 average same compare and frequency near6 (adjust change alter correct)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:41
S92	12	clock near2 (pulse edge) near8 count same compare and frequency near6 (adjust change alter correct)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:41
S93	24	clock near2 (pulses edges) near8 count same compare and frequency near6 (adjust change alter correct)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:44
S94	151	oscillator near2 (pulses edges) near10 count and frequency near6 (change alter match)	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:45
S95	63	oscillator near2 (pulses edges) near10 count and frequency near6 (change alter match) and display	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:45
S96	16	oscillator near2 (pulses edges) near10 count and frequency near6 (change alter match) and PLL	US-PGPUB; USPAT	OR	OFF	2005/10/06 16:49
S97	0	oscillator near2 (pulses edges) near10 count and frequency near6 (change alter match) and PLL	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:50
S98	1	clock near2 (pulses edges) near10 count and frequency near6 (change alter match) and PLL	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:50



## EAST Search History

S99	32	("4497036"   "4926166"   "5107188"   "5157308"   "5309168"   "5381043"   "5408500"   "5479183"   "5479184"   "5506545"   "5508714"   "5541646"   "5610955"   "5631920"   "5659339").PN. OR ("5757338"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/07 08:49
S10 0	4655	(SSM "spread spectrum modulation")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:01
S10 1	261	(SSM "spread spectrum modulation") and (LCD "liquid crystal display")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:01
S10 2	114	(SSM "spread spectrum modulation") and (LCD "liquid crystal display") and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:01
S10 3	77	(SSM "spread spectrum modulation") and (LCD "liquid crystal display") and feedback and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:05
S10 4	1755	clock near4 frequency and feedback and clock adj edge\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:05
S10 5	462	clock near4 frequency and feedback and clock adj edge\$2 same (count compare differ\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:06
S10 6	72	clock near4 frequency and feedback and clock adj edge\$2 same (count compare differ\$4) and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:07
S10 7	46	clock near4 frequency and feedback and clock adj edge\$2 same (count compare differ\$4) and display and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/22 10:07

## EAST Search History

S10 8	21050	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:44
S10 9	6246	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 0	2200	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 1	720	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 2	343	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and feedback and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 3	780	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 4	146	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 5	159	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:45
S11 6	75	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel\$2 and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:46
S11 7	62	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 and clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel\$2 and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:47

## EAST Search History

S11 8	541	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 same clock adj (edge\$2 pulse\$2) near6 count\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:47
S11 9	113	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 same clock adj (edge\$2 pulse\$2) near6 count\$4 and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:47
S12 0	113	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 same clock adj (edge\$2 pulse\$2) near6 count\$4 and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 17:04
S12 1	22	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 same clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:48
S12 2	24	clock\$2 same (adjust\$4 synchroniz\$4 match\$4) near6 frequenc\$4 same clock adj (edge\$2 pulse\$2) near6 count\$4 and pixel\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/23 16:48
S12 3	408	327/160.ccls.	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:53
S12 4	98	327/160.ccls. and clock adj pulses	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:53
S12 5	49	327/160.ccls. and clock adj pulses same count	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:53
S12 6	34	327/160.ccls. and clock adj pulses near6 count	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:53
S12 7	97	S123 and buffer	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:53
S12 8	16	S123 and buffer and clock adj pulses	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:54
S12 9	11	S123 and buffer and clock adj pulses same count\$6	US-PGPUB; USPAT	OR	OFF	2006/03/24 10:54
S13 0	5410	frequency near6 (adjust change) and count\$4 near4 (pulses edges)	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:29
S13 1	1922	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and display	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:29
S13 2	692	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and display and buffer	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:29

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S13 3	380	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and display and buffer and feedback	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:45
S13 4	885	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and buffer and feedback	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:45
S13 5	366	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and buffer and feedback same clock	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:46
S13 6	69	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and buffer and feedback same clock and coordinates	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:45
S13 7	74	frequency near6 (adjust change) and count\$4 near4 (pulses edges) and buffer and feedback same clock and image\$2	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:50
S13 8	283	(PLL "phase locked loop") and count\$4 near4 (pulses edges) same feedback	US-PGPUB; USPAT	OR	OFF	2006/04/05 13:50
S13 9	261	(PLL "phase locked loop") and count\$4 near4 (pulses edges) same feedback and clock	US-PGPUB; USPAT	OR	OFF	2006/04/05 14:03
S14 0	216	(PLL "phase locked loop") and count\$4 near4 (pulses edges) same feedback and (pulses edges) same compar\$4	US-PGPUB; USPAT	OR	OFF	2006/04/05 14:03
S14 1	213	(PLL "phase locked loop") and count\$4 near4 (pulses edges) same feedback and (pulses edges) same (compar\$4 differen\$3) same feedback	US-PGPUB; USPAT	OR	OFF	2006/04/05 14:13
S14 2	94	(PLL "phase locked loop") and count\$4 near4 (pulses edges) same feedback and (pulses edges) same (differen\$3) same feedback	US-PGPUB; USPAT	OR	OFF	2006/04/05 14:13
S14 3	22	"graphic memory controller hub"	US-PGPUB; USPAT	OR	OFF	2006/04/06 17:06
S14 4	1	"graphic memory controller hub" and DRAM	US-PGPUB; USPAT	OR	OFF	2006/04/06 17:03
S14 5	1838	DRAM and computer and graphics adj controller	US-PGPUB; USPAT	OR	OFF	2006/04/06 17:06
S14 6	506	DRAM and computer and graphics adj controller same DRAM	US-PGPUB; USPAT	OR	OFF	2006/04/06 17:07

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S14 7	506	computer and graphics adj controller same DRAM	US-PGPUB; USPAT	OR	OFF	2006/04/06 17:07
S14 8	1	"5007070".pn.	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:08
S14 9	13020	"phase locked loop" and "voltage controlled oscillator"	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:09
S15 0	8331	"phase locked loop" same "voltage controlled oscillator"	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:09
S15 1	13	"phase locked loop" same "voltage controlled oscillator" and pluses	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:09
S15 2	3684	"phase locked loop" same "voltage controlled oscillator" and pulses	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:09
S15 3	1698	"phase locked loop" same "voltage controlled oscillator" and pulses same count\$4	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:09
S15 4	914	"phase locked loop" same "voltage controlled oscillator" and pulses same count\$4 and feedback	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:10
S15 5	438	"phase locked loop" same "voltage controlled oscillator" and pulses same count\$4 and feedback and buffer	US-PGPUB; USPAT	OR	OFF	2006/04/06 18:10
S15 6	1508	laptop adj computer and DRAM	US-PGPUB; USPAT	OR	OFF	2006/04/07 10:01
S15 7	483	laptop adj computer and DRAM	USPAT	OR	OFF	2006/04/07 10:01